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AMENDMENTS TO THE SPECIFICATION

Please amend the paragraph beginning at page 6, line 5 as follows:

Referring to Figure 2, a first embodiment of a transmitter circuit 20 for I/Q modulation, frequency translation, and pulse shaping is shown. Transmitter circuit 20 includes mapper 22 which will typically include a look up table for generating each of an I-channel baud rate data signal 21(I) and a Q-channel baud rate data signal 21(Q) using techniques known in the art. In the exemplary embodiment, each data signal is a 7-bit value and the data is either at a 2MHz or 4MHz baud rate. However, both the 2Mbaud and the 4Mbaud data are represented by a data signal comprising a series of data values clocked at 4MHz, every other data value being a zero for the 2Mbaud data. The I-channel data signal and the Q-channel data signal are each input to a pre-scaler 24(I) and 24(Q) respectively. Prescaler 24(I) multiplies the I-channel data signal by a scaler value from a multiplexer 26 25 and similarly, prescaler 24(Q) multiplies the Q-channel data signal by a scaler value from the multiplexer 26 25. The multiplexer 25 selects between scaler values of 1 and 2/3 based on an input baud rate signal. When the baud rate is 2MHz the selected scaler value is 1 and when the baud rate is 4MHz, the selected scaler value is 2/3. The purpose of scaling the I-channel data signal and the Q-channel data signal is to assure that the total signal strength remains within the required range independent of the baud rate.